

Fig.1 System flow chart

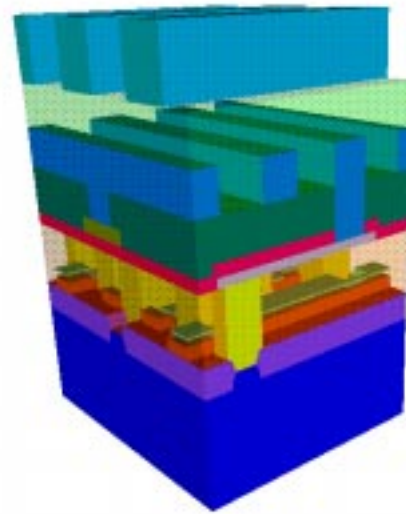


Fig.2 3D SRAM rendering with transparent layers

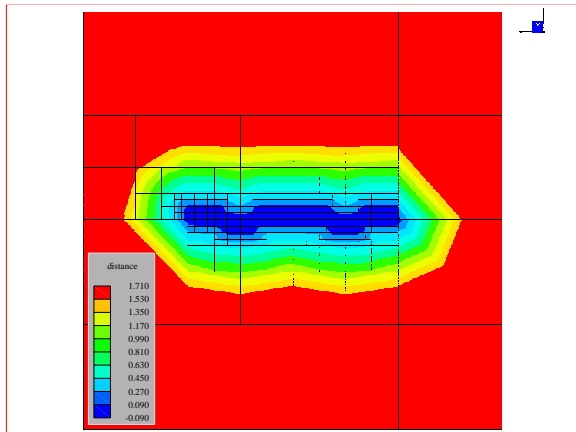


Fig.3 Distance contour and octree mesh sections for word line

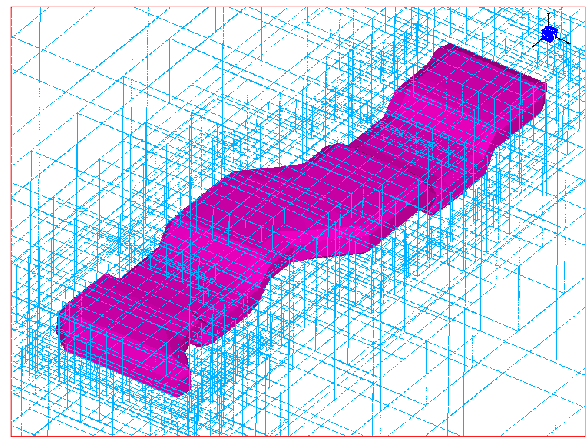


Fig.4 Iso-surface and octree mesh for word line

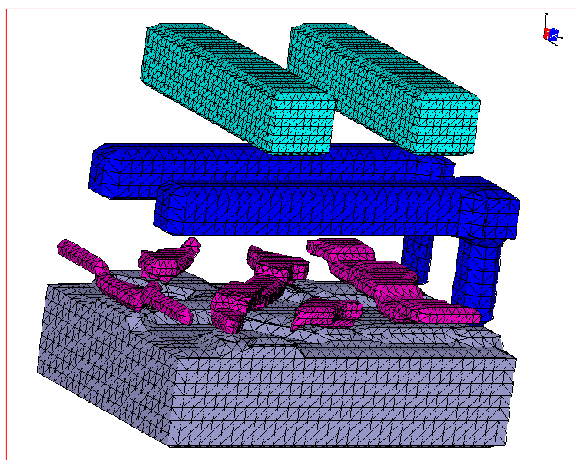


Fig.5 Silicon substrate, polysilicon layer, first and second metal layers in surface mesh

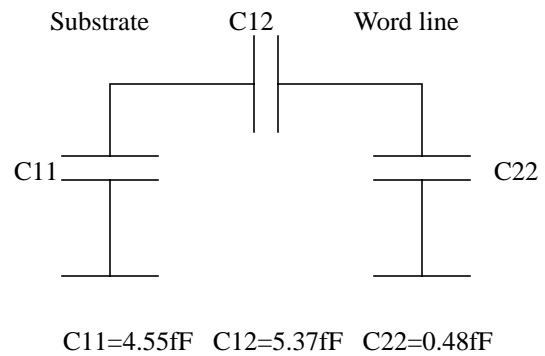


Fig.6 Capacitance for a single word line and substrate