

# *The Equivalence of van der Ziel and BSIM4 Models in Modeling the Induced Gate Noise of MOSFETs*

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*Center for Integrated Systems*  
*Stanford University*

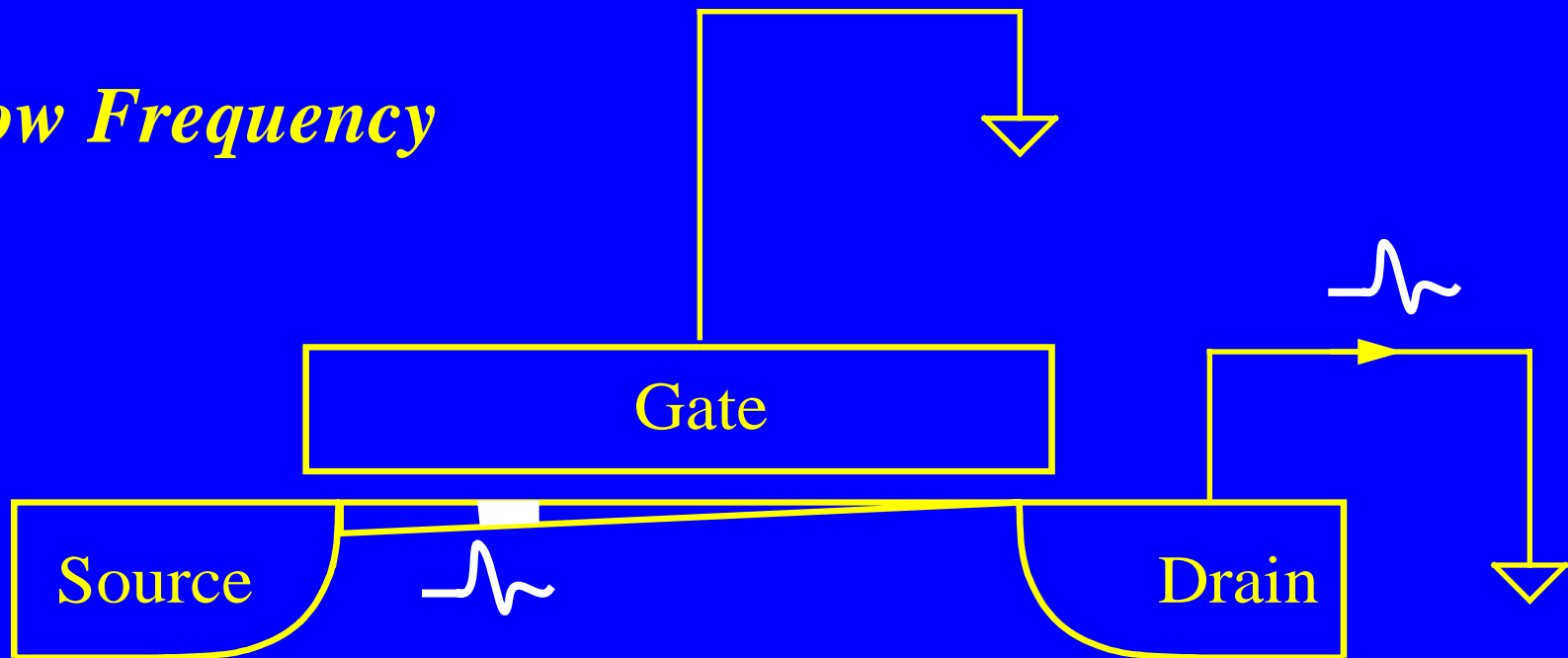
# Outline

- Induced Gate Noise
- BSIM4 Noise Model
- Model Comparison Method
- Validation for Device
- Validation for Circuits
- Conclusions



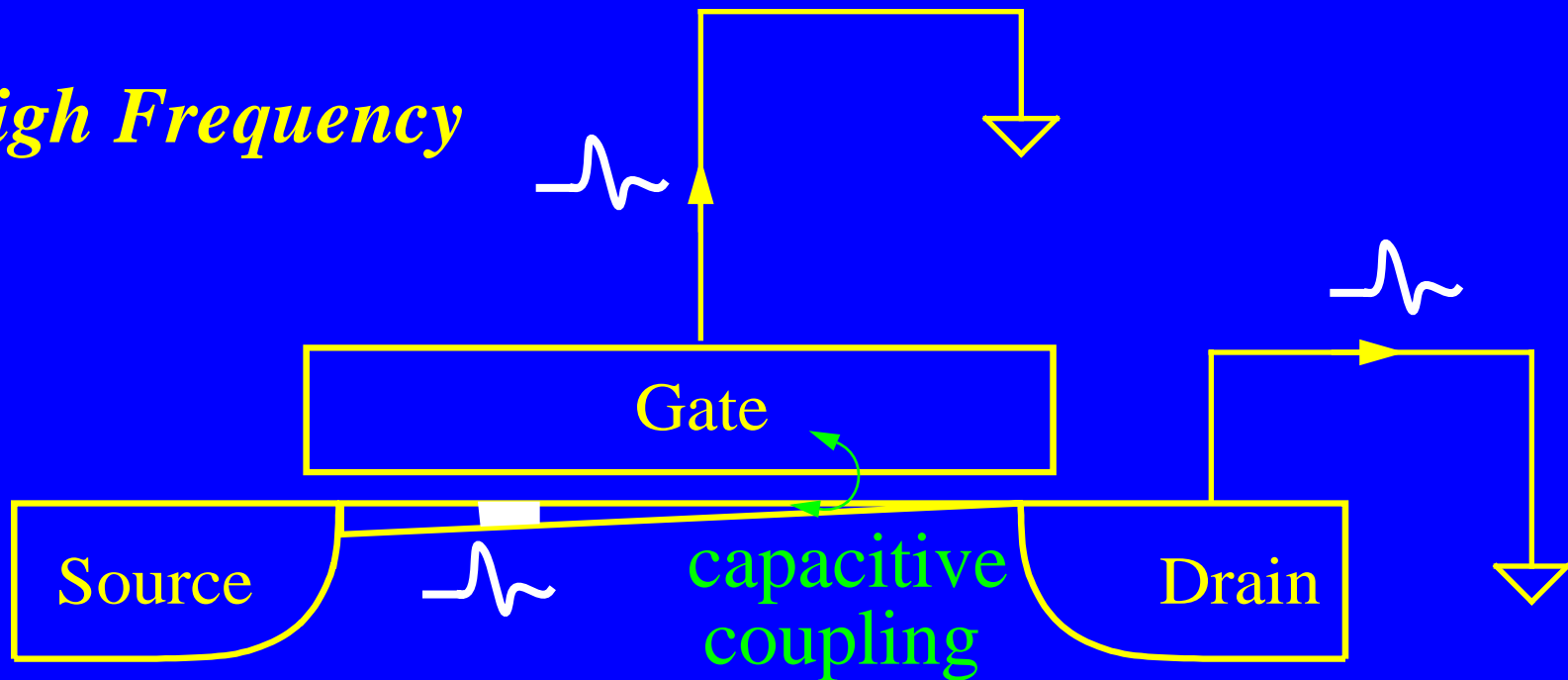
# Induced Gate Noise (Physical Origin)

*At Low Frequency*



# Induced Gate Noise (Physical Origin)

*At High Frequency*



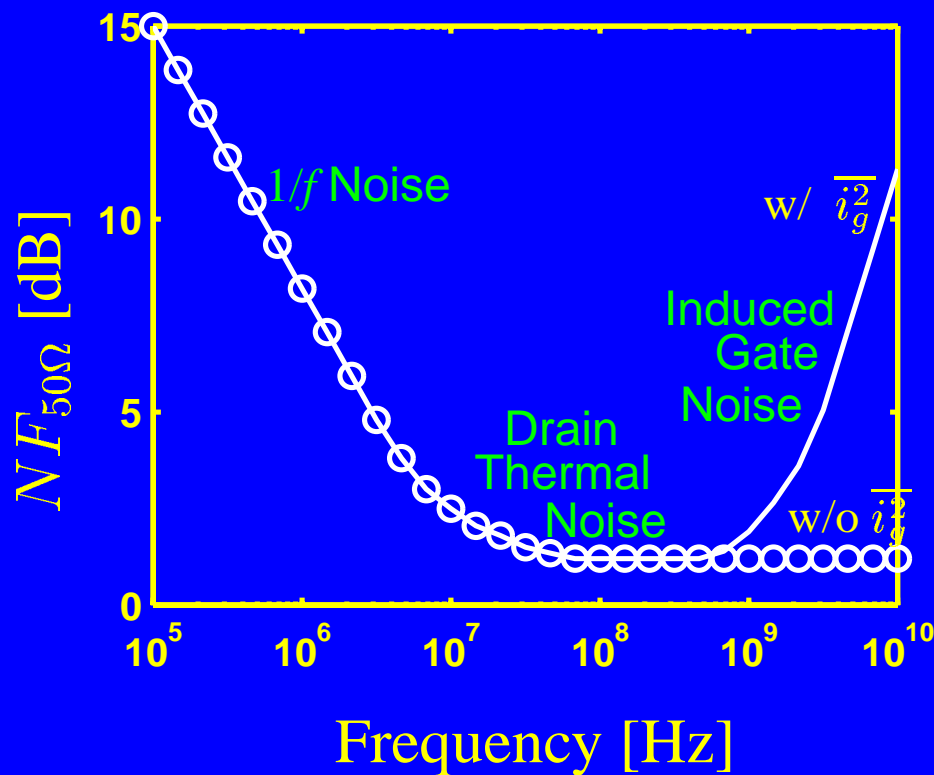
Noticeable  $\overline{i_g^2}$  if  $f > f_t/10$



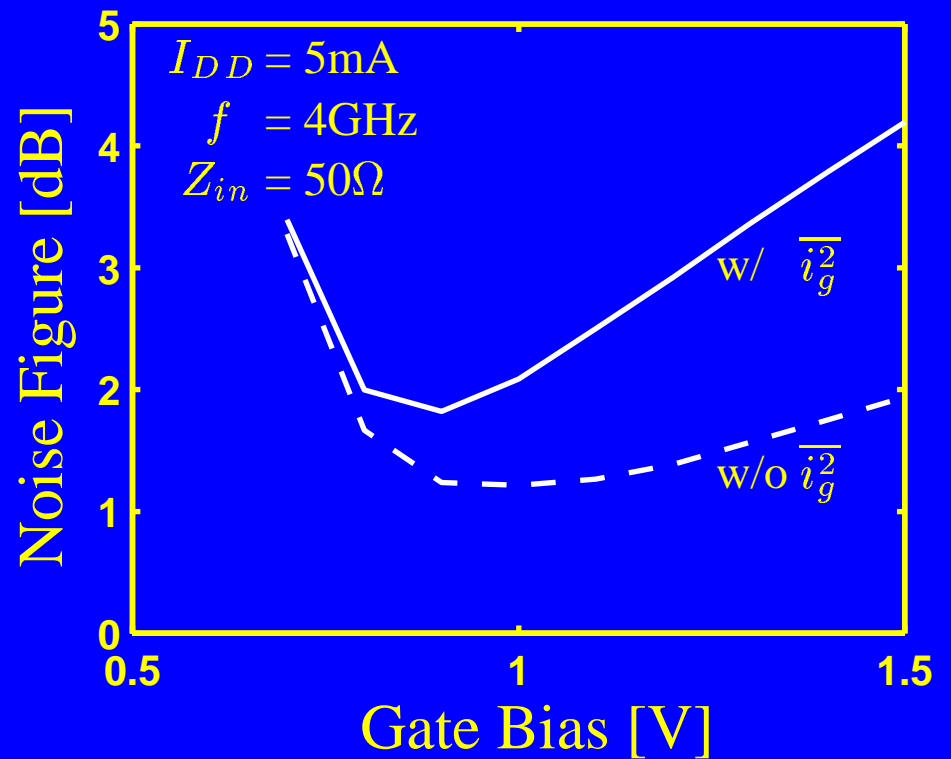
# Induced Gate Noise *(Continue)*

## (Impact on Noise Figure)

MOSFET \*  $NF_{50\Omega}$



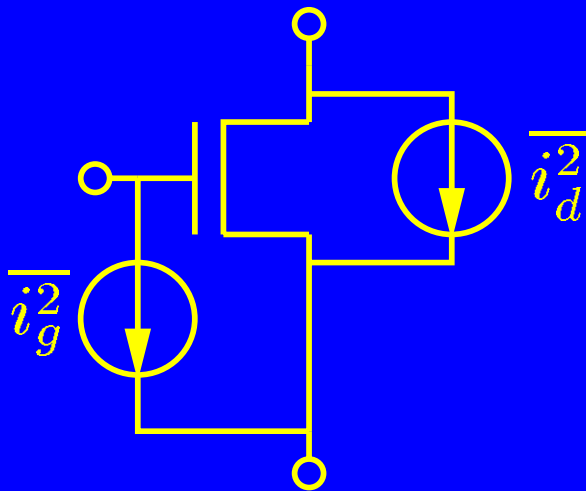
Power-Constrained LNA



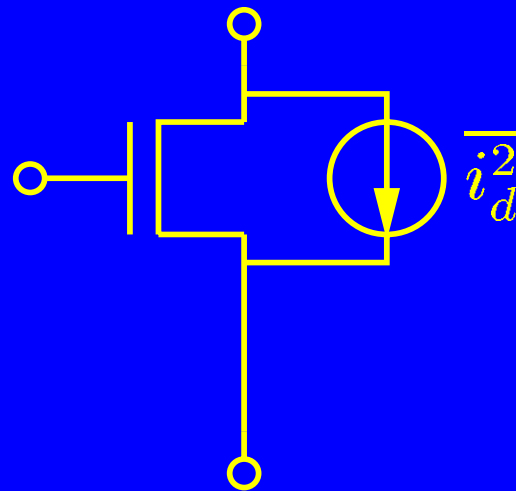
# Induced Gate Noise *(Continue)*

## (SPICE Models)

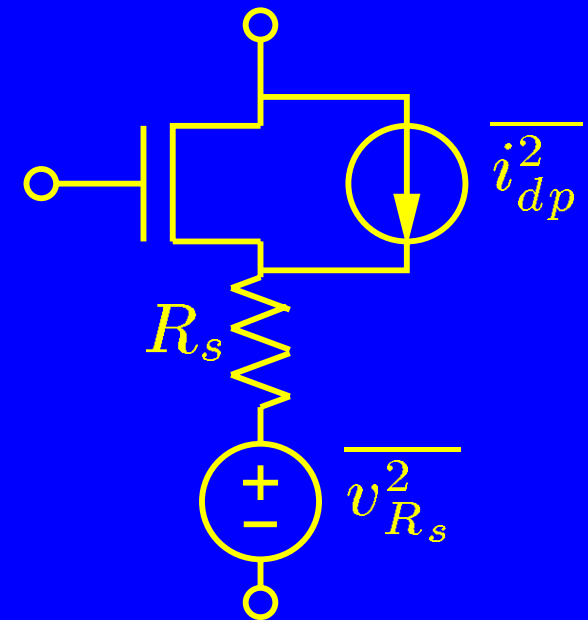
**van der Ziel**



**BSIM3**



**BSIM4**



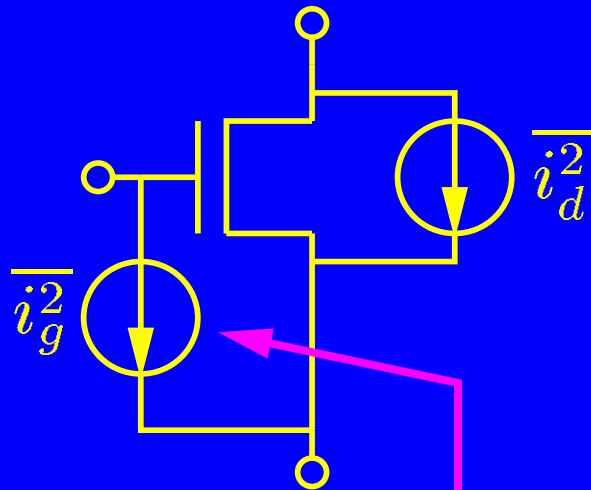
- \* **Solid Physical Basis**
- \* *de facto* **Standard**
- \* **Released in Mar.2000**
- \* **No Induced Gate Noise**
- \* **New Approach**



# BSIM4 Noise Model

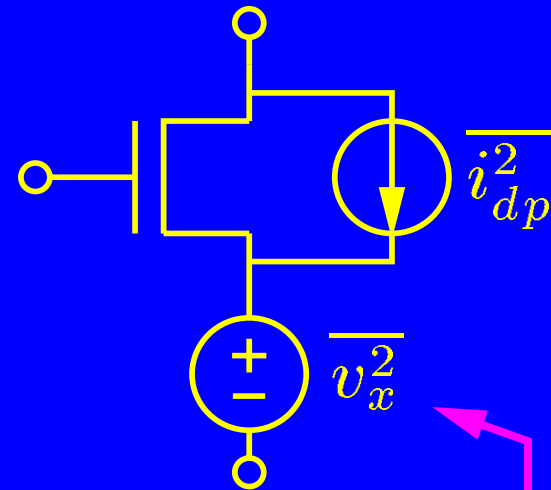
## (Modeling Strategy)

van der Ziel



\* Connected to **Gate**

BSIM4



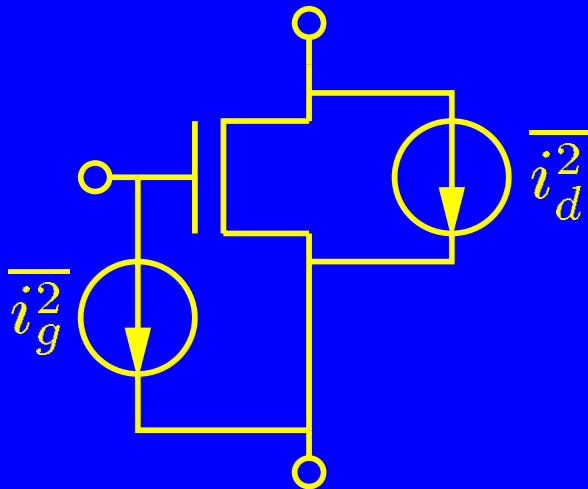
\* Connected to **Source**



# BSIM4 Noise Model

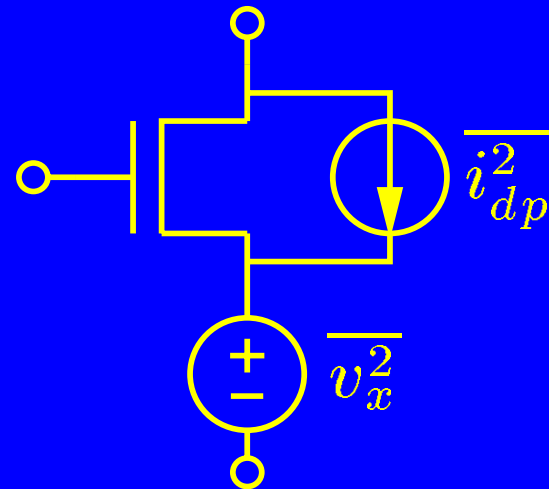
## (Modeling Strategy)

van der Ziel



- \* Connected to Gate
- \* Frequency **Dependent**

BSIM4



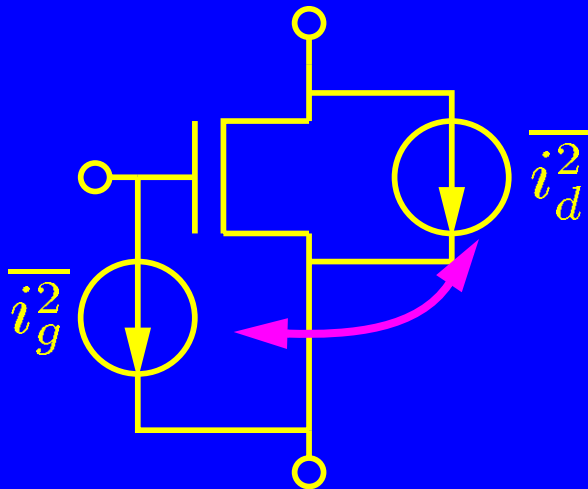
- \* Connected to Source
- \* Frequency **Independent**



# BSIM4 Noise Model

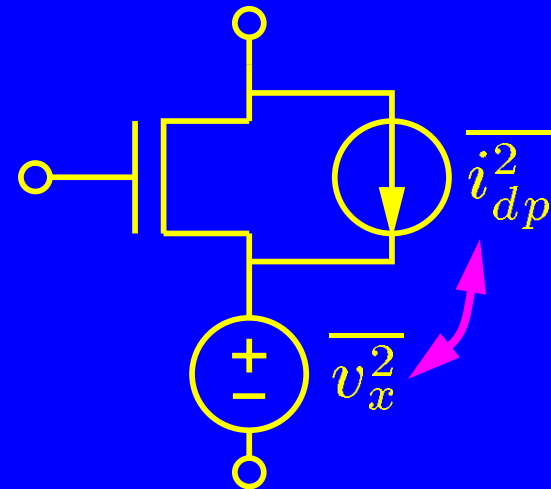
## (Modeling Strategy)

### van der Ziel



- \* Connected to Gate
- \* Frequency Dependent
- \* **Correlated** with  $i_d^2$   
(3 Noise Parameters)

### BSIM4



- \* Connected to Source
- \* Frequency Independent
- \* **Uncorrelated** with  $i_{dp}^2$   
(2 Noise Parameters)



# BSIM4 Noise Model *(Continue)*

## (Arguments)

- BSIM4 is easy to implement.
- What is the physical relationship to the van der Ziel model ?
- How to extract the parameters ?
- What is the underlying assumption ?
- How small is the discrepancy ?

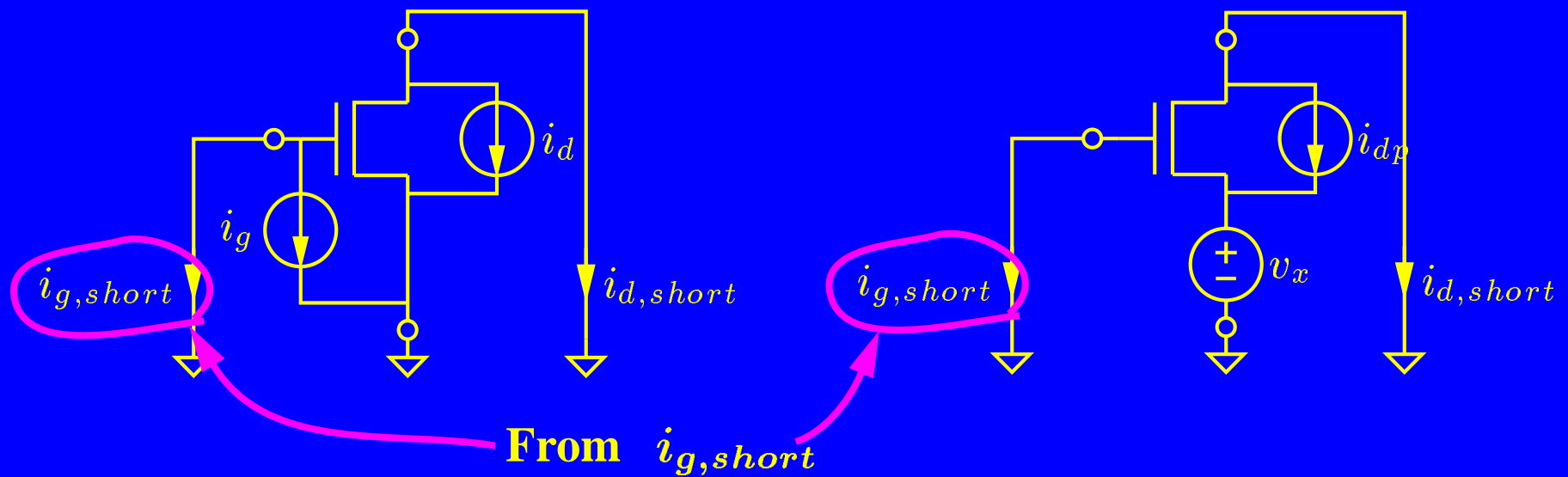


# BSIM4 Noise Model (Continue)

## (Physical Relationship to van der Ziel)

van der Ziel

BSIM4



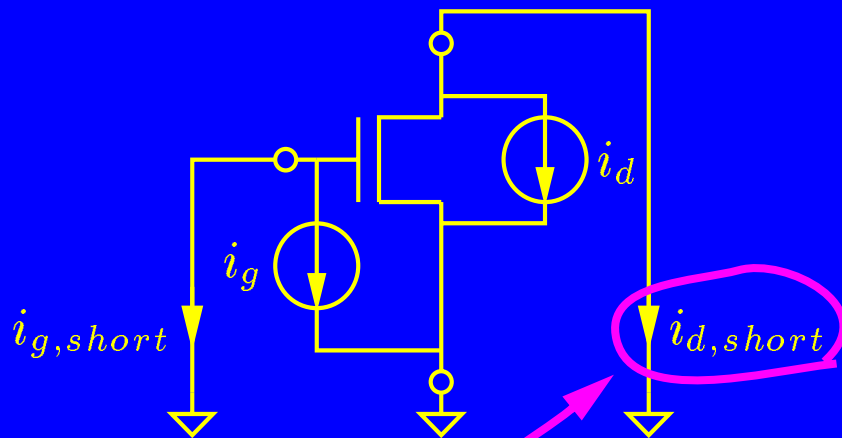
$$i_g = -(Y_{11} + Y_{12})v_x$$



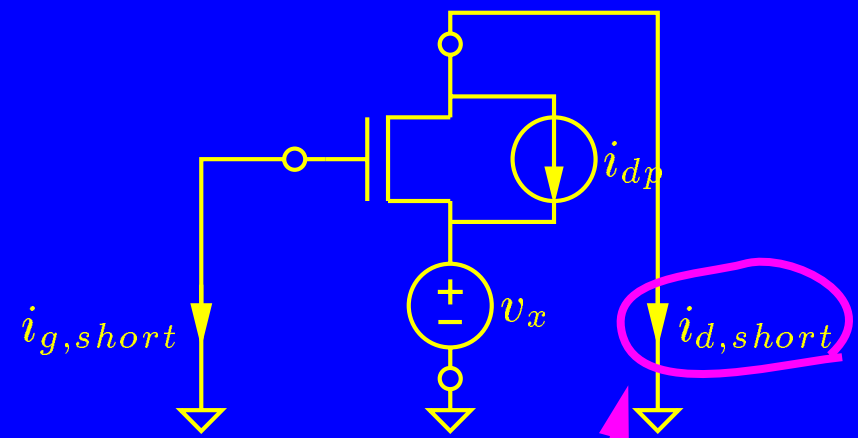
# BSIM4 Noise Model *(Continue)*

## (Physical Relationship to van der Ziel)

van der Ziel



BSIM4



From  $i_{g,short}$

$$i_g = -(Y_{11} + Y_{12})v_x$$

From  $i_{d,short}$

$$i_d = -(Y_{21} + Y_{22})v_x + i_{dp}$$



# BSIM4 Noise Model *(Continue)*

## (Physical Relationship to van der Ziel)

### Exact Fit

$$\overline{v_x^2} = \overline{i_g^2} / |Y_{11} + Y_{12}|^2$$

$$\overline{i_{dp}^2} = \overline{i_d^2} - |Y_{21} + Y_{22}|^2 \overline{v_x^2}$$



# BSIM4 Noise Model *(Continue)*

## (Physical Relationship to van der Ziel)

### Exact Fit

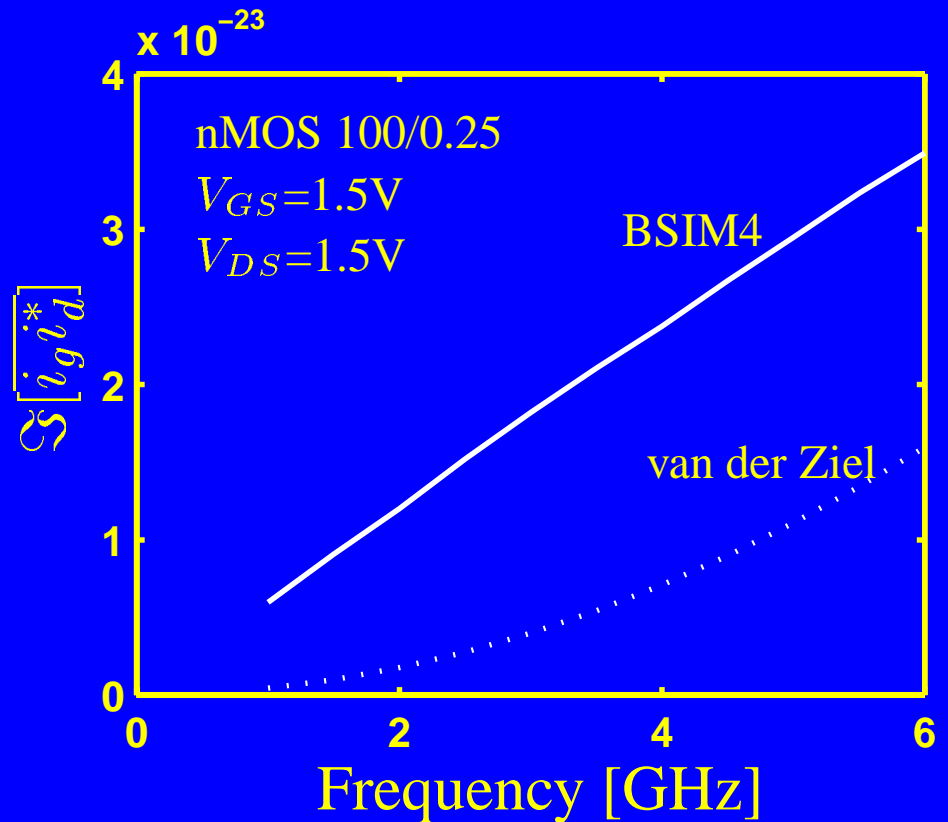
$$\overline{v_x^2} = \overline{i_g^2} / |Y_{11} + Y_{12}|^2$$

$$\overline{i_{dp}^2} = \overline{i_d^2} - |Y_{21} + Y_{22}|^2 \overline{v_x^2}$$

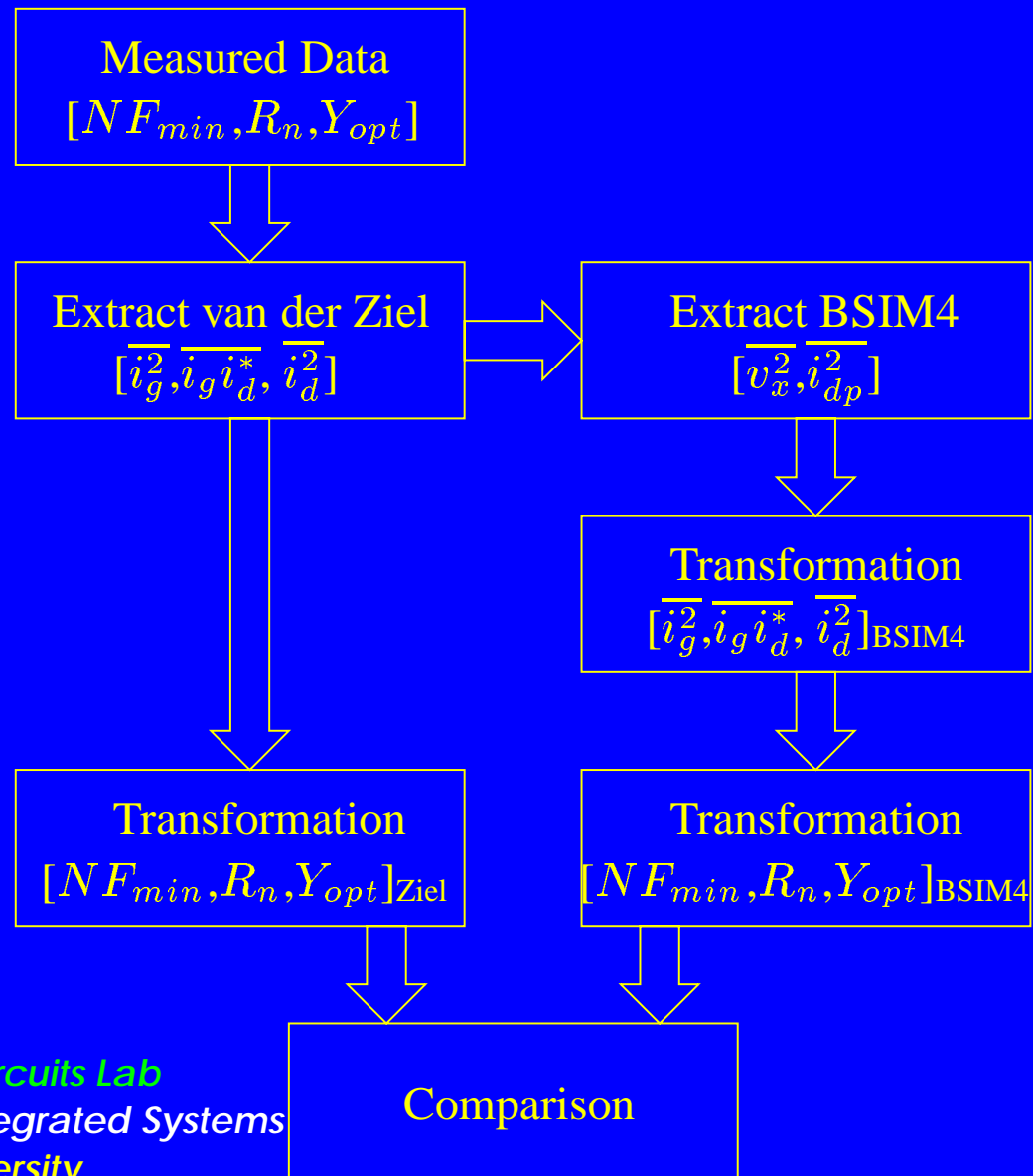
### Discrepancy

$$\overline{i_g i_d^*} = \overline{i_g^2} (Y_{21} + Y_{22})^* / (Y_{11} + Y_{12})^*$$

is assumed in BSIM4



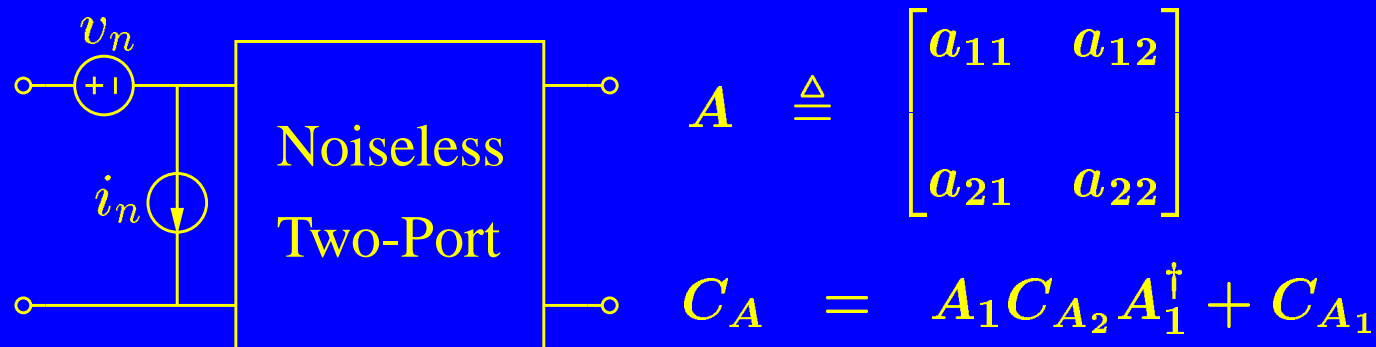
# Model Comparison Method (Parameter Extraction Procedure)



# Model Comparison Method *(Continue)*

## (Noisy Two-Port Theory)

### Cascade Connection (Chain Parameter)



$$C_A \triangleq \begin{bmatrix} C_{v_n v_n^*} & C_{v_n i_n^*} \\ C_{i_n v_n^*} & C_{i_n i_n^*} \end{bmatrix}$$

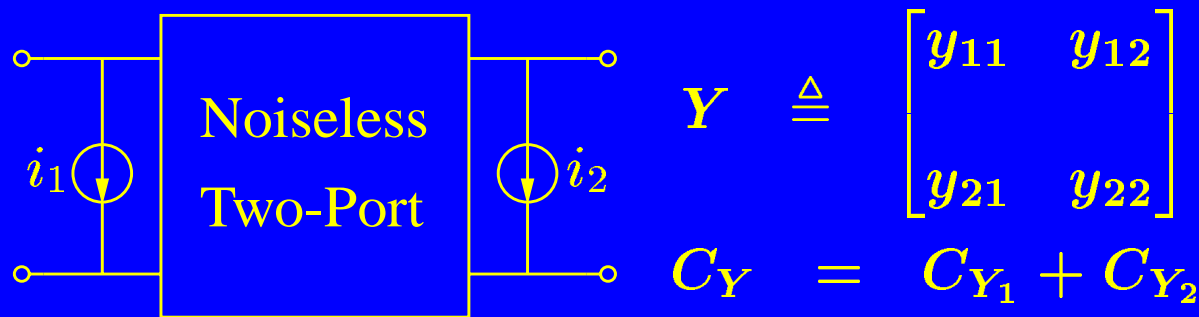
$$= 2kT \begin{bmatrix} R_n & \frac{F_{min}-1}{2} - R_n Y_{opt}^* \\ \frac{F_{min}-1}{2} - R_n Y_{opt} & R_n |Y_{opt}|^2 \end{bmatrix}$$



# Model Comparison Method *(Continue)*

## (Noisy Two-Port Theory)

### Parallel Connection (*Y* Parameter)



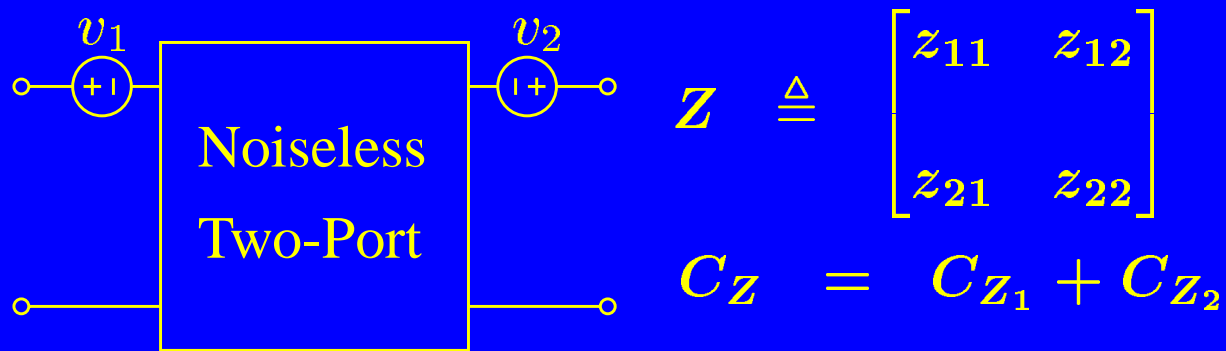
$$C_Y \triangleq \begin{bmatrix} C_{i_1 i_1^*} & C_{i_1 i_2^*} \\ C_{i_2 i_1^*} & C_{i_2 i_2^*} \end{bmatrix}$$
$$= 2kT\Re[Y]$$



# Model Comparison Method *(Continue)*

## (Noisy Two-Port Theory)

### Series Connection (Z Parameter)

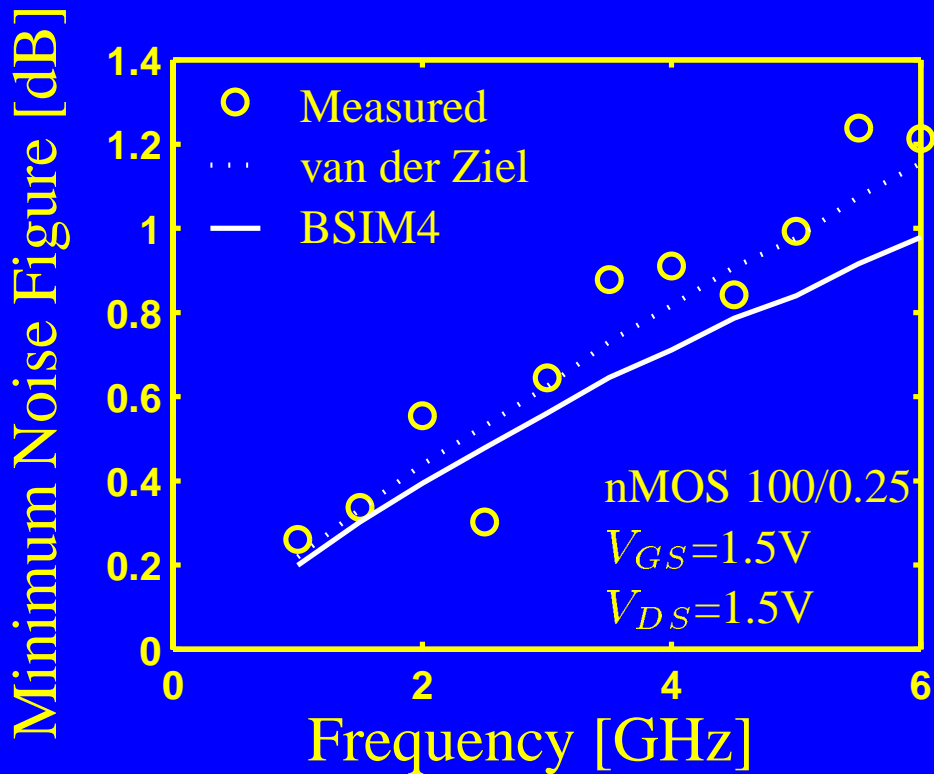


$$\mathbf{C}_Z \triangleq \begin{bmatrix} C_{v_1 v_1^*} & C_{v_1 v_2^*} \\ C_{v_2 v_1^*} & C_{v_2 v_2^*} \end{bmatrix}$$
$$= 2kT\Re[\mathbf{Z}]$$

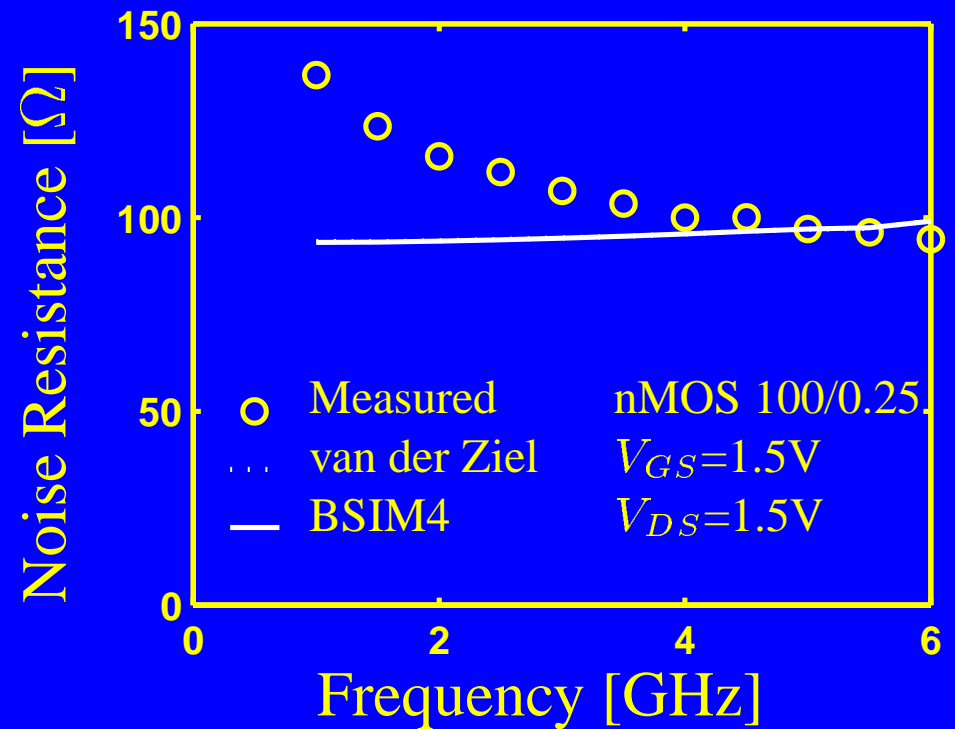


# Validation for Device (4 Noise Parameters)

$NF_{min}$



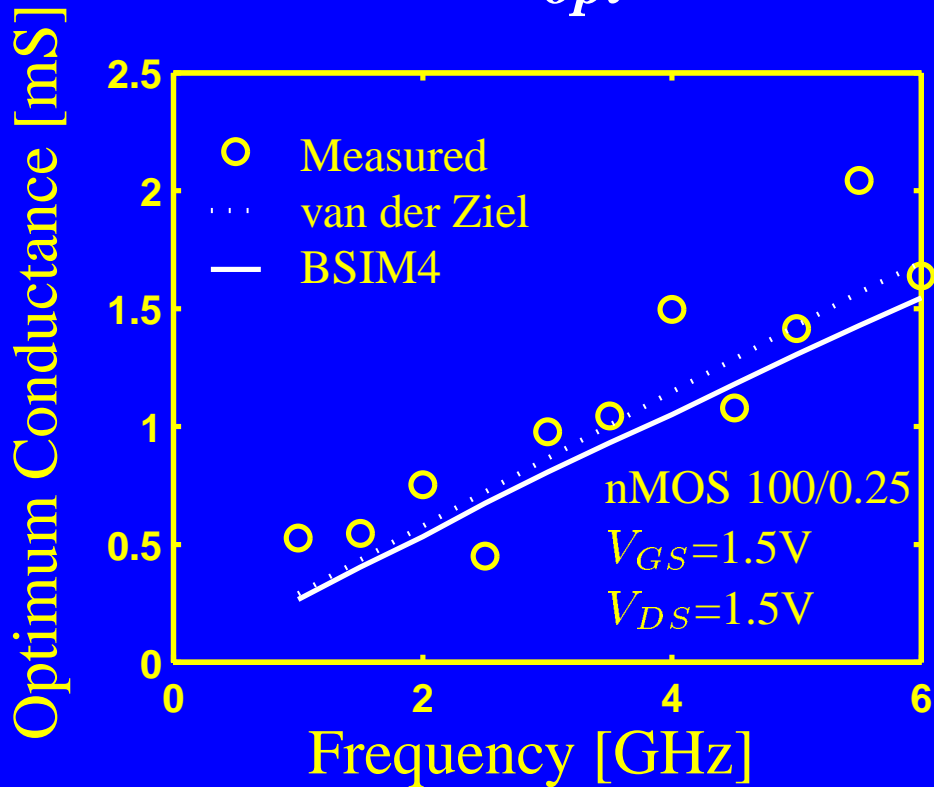
$R_n$



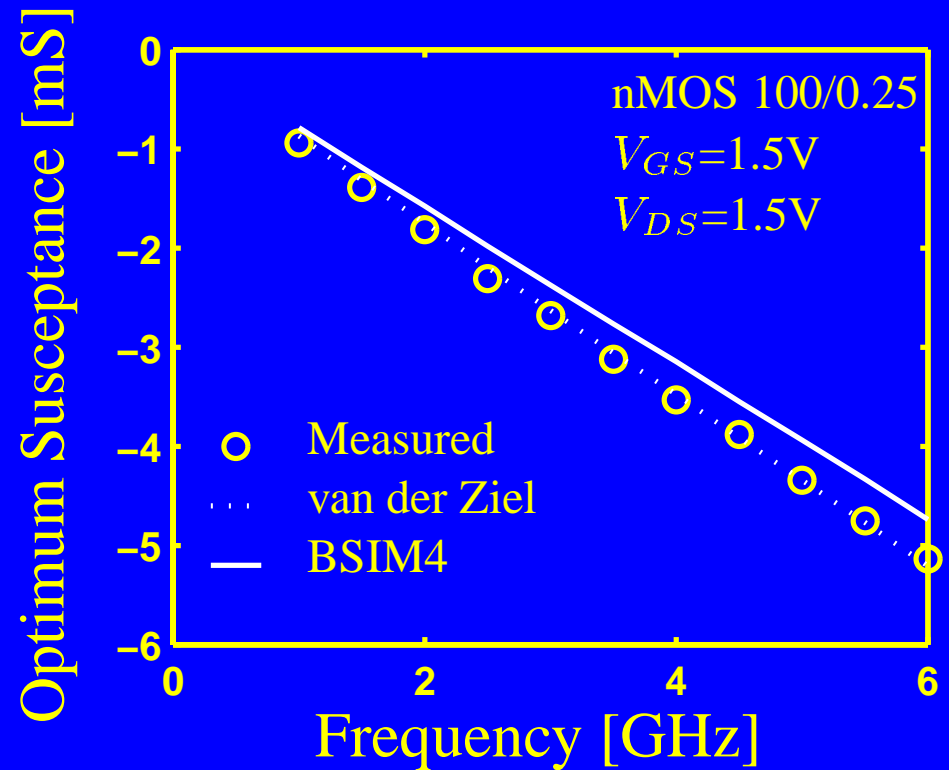
# Validation for Device *(Continue)*

## (4 Noise Parameters)

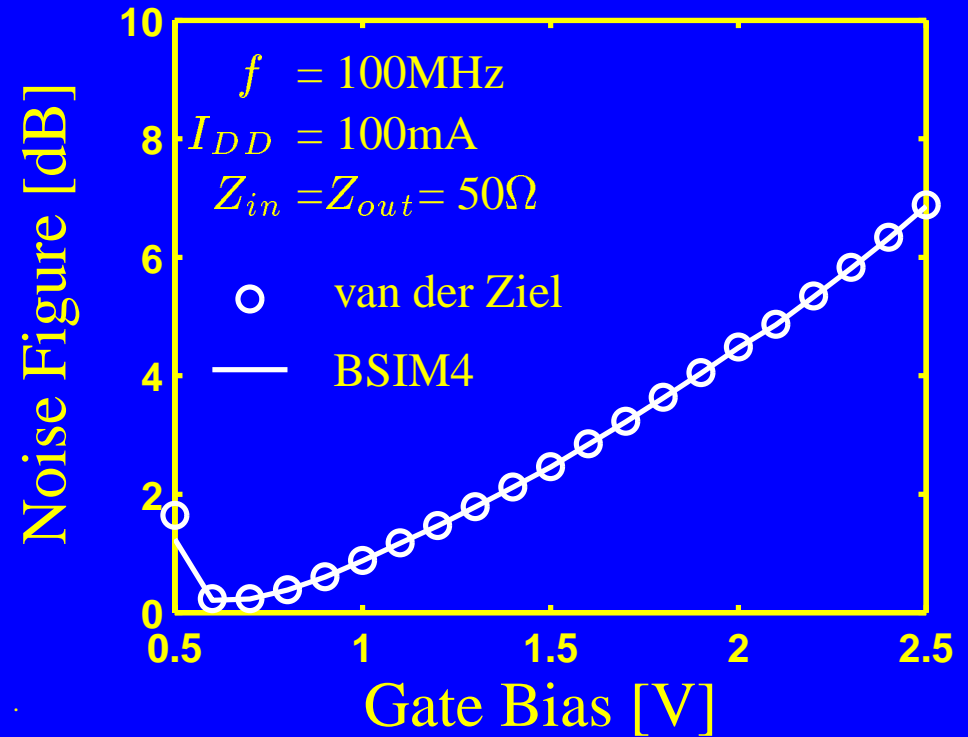
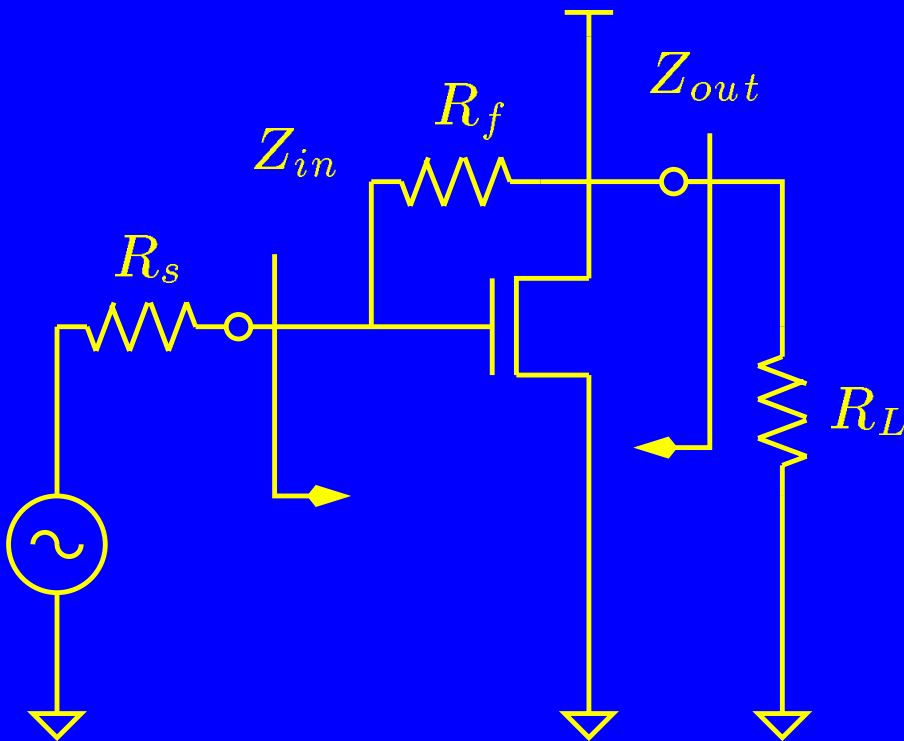
$G_{opt}$



$B_{opt}$



# Validation for Circuits (Shunt-series Feedback Amplifier)



$$NF \triangleq \overset{\sim 0}{NF_{min}} + \frac{(Y_s - Y_{opt})^2 R_n}{G_s Y_s} \gg Y_{opt}$$

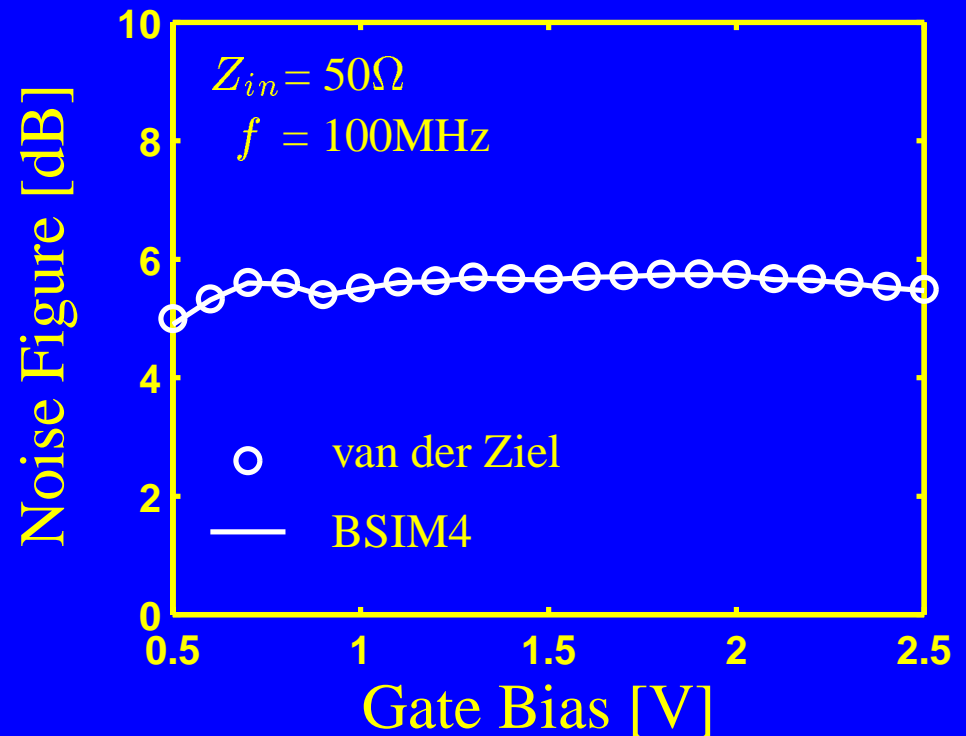
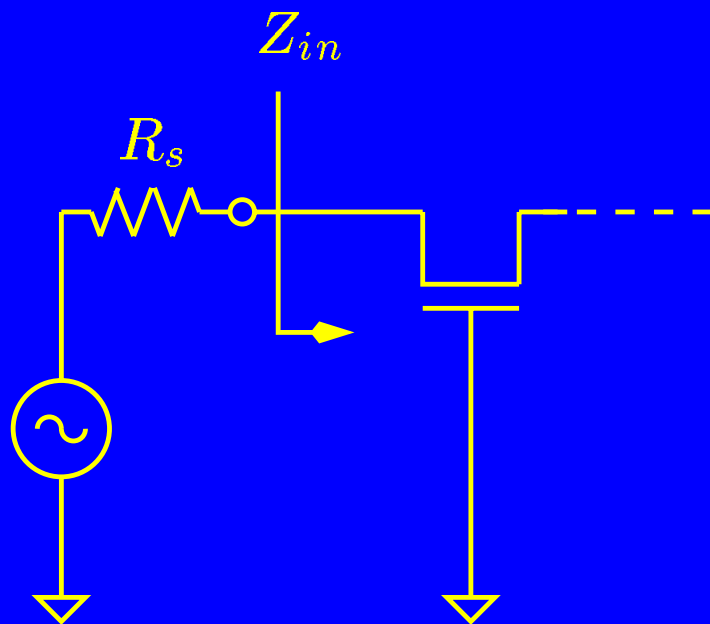
$$\approx \frac{|Y_s|^2 R_n}{G_s} \text{ Same}$$

Constant



# Validation for Circuits *(Continue)*

## $(1/g_m$ Termination Amplifier)

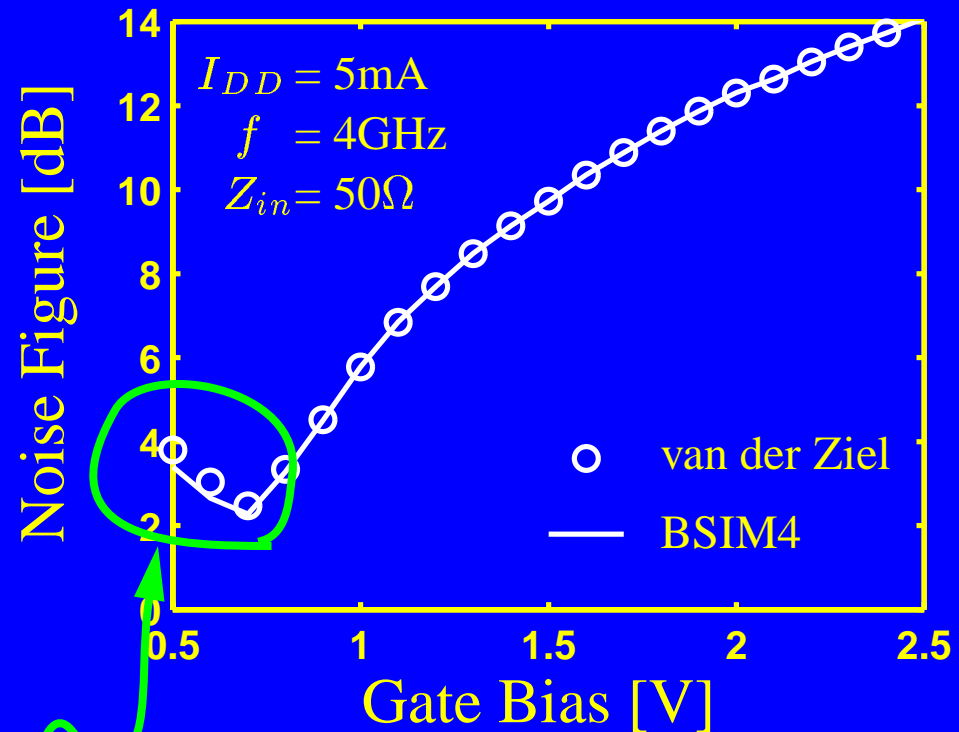
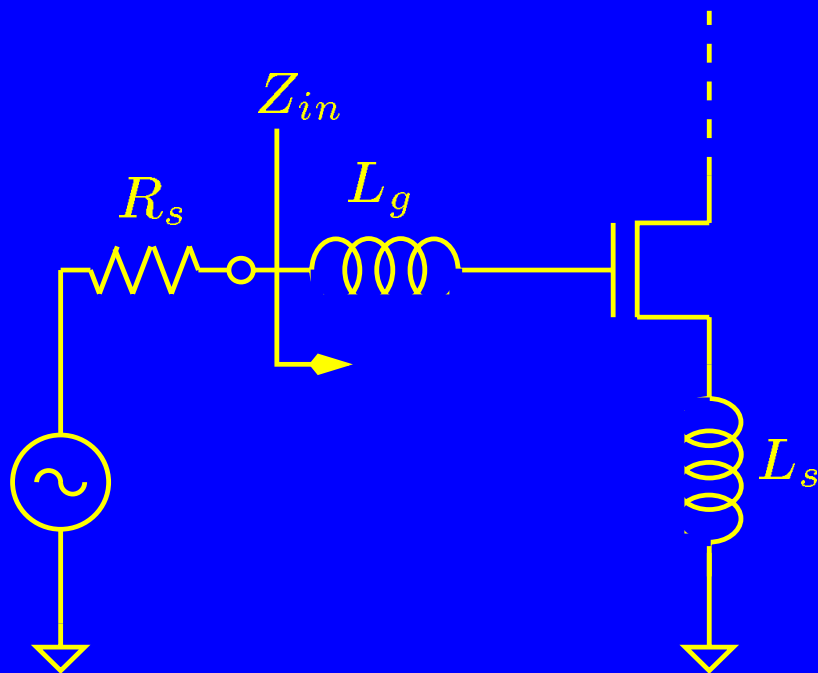


- \* By the same reason.
- \* Validates a non-grounded source electrode condition.



# Validation for Circuits *(Continue)*

## (Inductive Degeneration Tuned Amplifier)

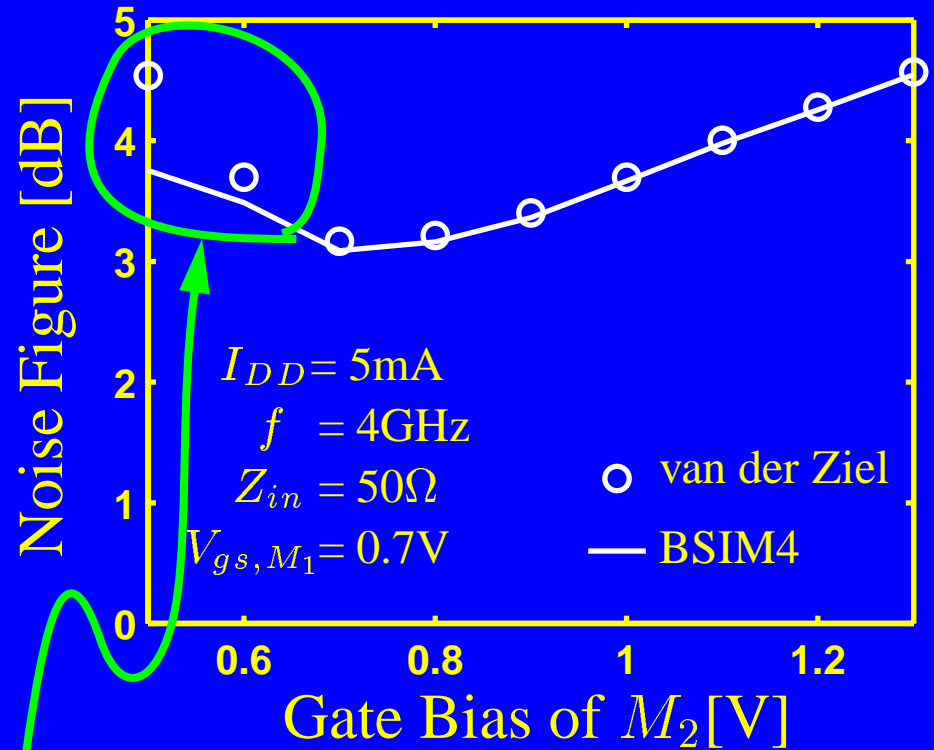
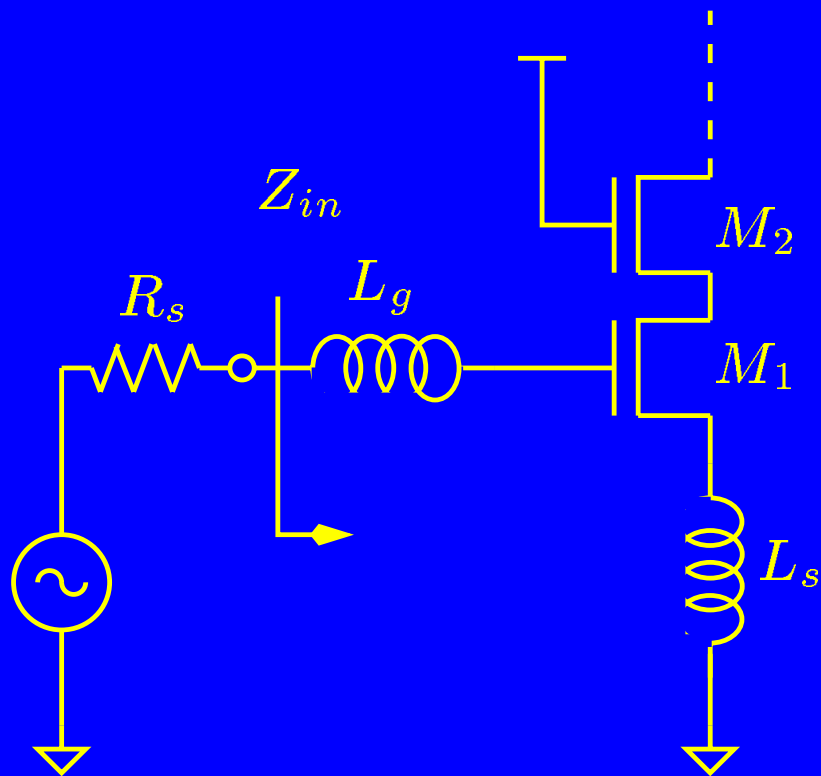


- \*  $Y_{opt}$  is comparable to  $Y_s$ .
- \* Another non-grounded source electrode case.



# Validation for Circuits *(Continue)*

## (Tuned Amplifier with Cascode Stage)



\* More noticeable discrepancy.



# Conclusions

- First independent comparison between BSIM4 and van der Ziel models for induced gate noise.
- BSIM4 reproduces the van der Ziel model but introduces errors in the correlated term.
- In practical circuits, noticeable errors arise for very low gate bias conditions only.
- The two models are equivalent in most practical circuits.

